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 -- Figure 6 is a schematic view of another pulse width modulation amplifier with a feedback filter in which the present invention could be utilized. --

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Please add the following new paragraph immediately following paragraph 46 in the

DESCRIPTION OF THE PRESENT INVENTION.

*Ant*  
*D2*

-- As illustrated in Figure 6, the present invention also provides a power amplifier system 40 for driving a load 45. The power amplifier system 40 includes an input terminal for receiving a signal from a signal generator, an output terminal connected to the load 45, a pulse width modulation circuit 42 creating ripple spectra, an error amplifier and modulator 43 connected to an input of the pulse width modulation circuit 42, a demodulation filter 47 connected to an output of the pulse width modulation circuit 42, and a feedback control loop coupled to the pulse width modulation circuit 42 and including an active low-pass filter having a feedback demodulation filter 44 and an isolated integrator frequency-rejecting network. In one embodiment, the isolated-integrator frequency-rejecting network is an isolated-integrator band-reject filter. --

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